

Radio Front-Ends for 100 Gbps and beyond

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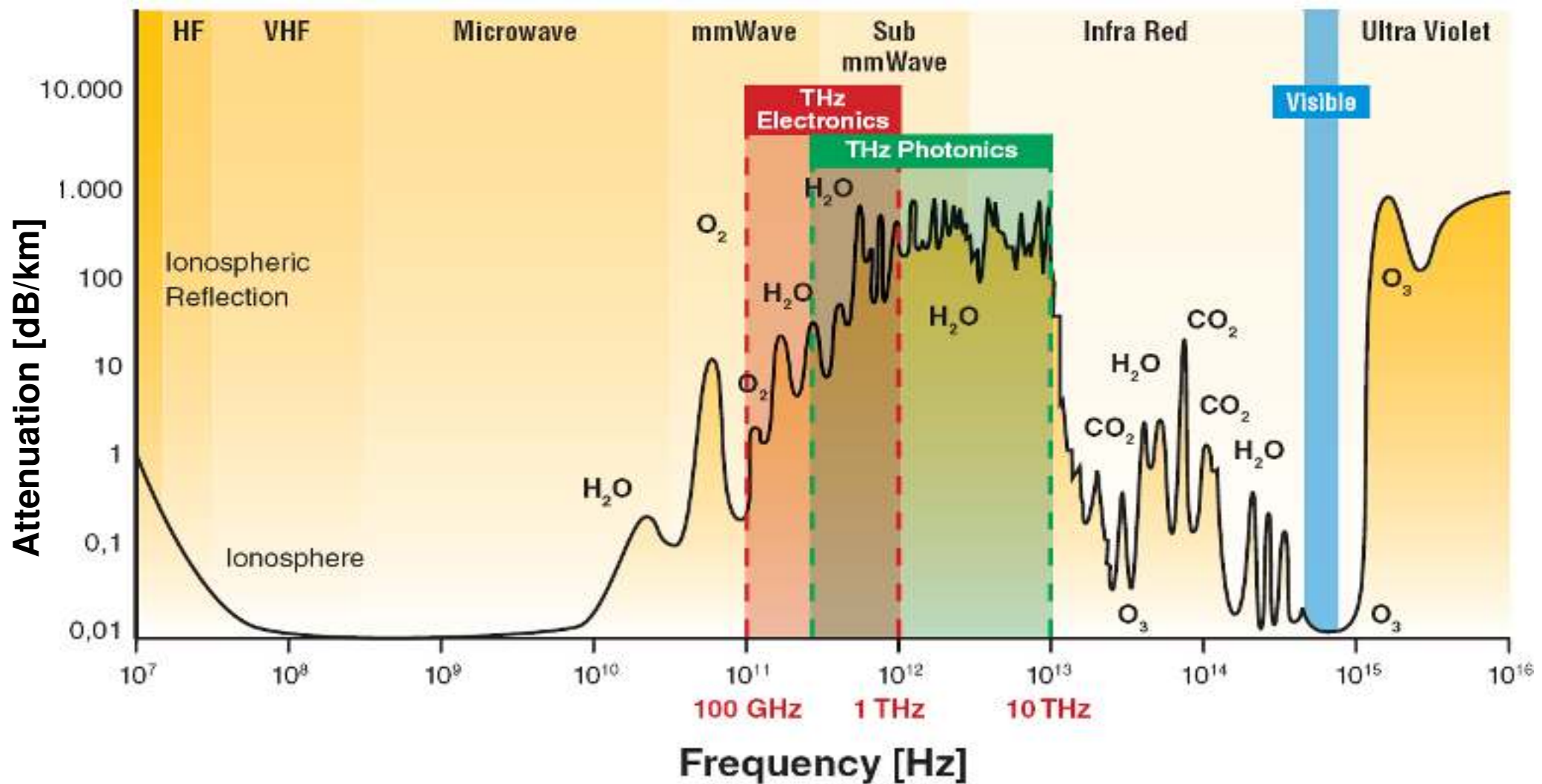
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High-Frequency and Communication Technology (IHCT)

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The Pursuit to Unlimited Bandwidth



mmWave/THz Application Scenarios



mmWave, THz Imaging and Sensing

- Non-destructive testing
- Security
- Bio-medical
- Space
- Material characterization
- Spectroscopy
- Super-Resolution Imaging



Radar Applications

- 77GHz, 120GHz, 240GHz
- Automotive
- Remote control
- Gesture recognition
- Process control
- 3-D Imaging
- Patient monitoring



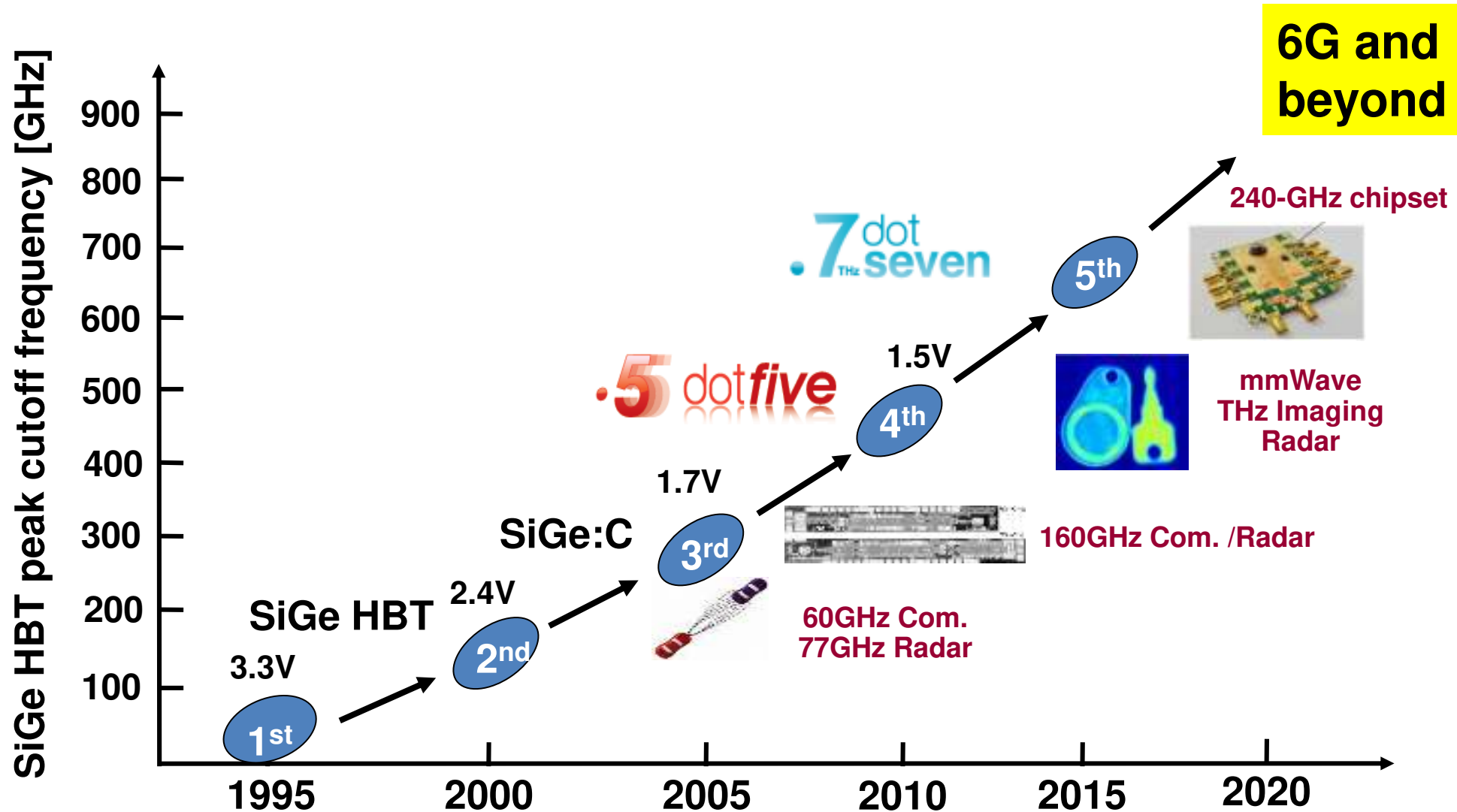
High-speed Communication

- 60GHz, E-band, 5G, 6G and beyond
- **IEEE 802.15.3d-2017**
- **252-322 GHz**
- Towards 100Gbit/s
- Interconnects
- Data servers
- Networking and protocols



Can we use silicon to implement THz Radio Front-Ends?

Silicon (SiGe) HBT Technology Evolution



240GHz link-budget estimation (QPSK)

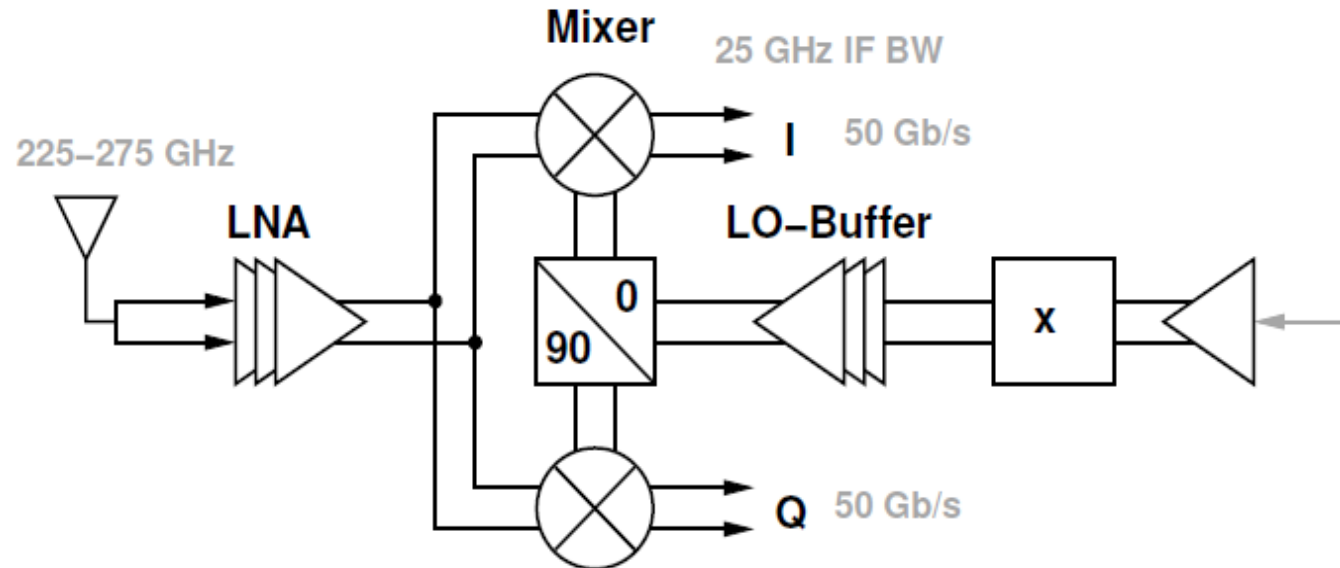
$$FSPL_{max} = 10 \log_{10}(kT \cdot BW) + NF + SNR_{min} - P_{TX} - G_{AntTX} - G_{AntRX} + 10dB$$

Tx power	NF	Number of channels	Band width	Minimum required receive power	Tx antenna gain	Rx antenna gain	Maximum path loss	Achievable range
0 dBm	20 dB	1	50 GHz	-39,84 dBm	0 dBi	0 dBi	29,84 dB	0,003 m
3 dBm	10 dB	1	50 GHz	-49,84 dBm	25 dBi	5 dBi	72,84 dB	0,44 m
6 dBm	10 dB	2	25 GHz	-52,85 dBm	25 dBi	5 dBi	78,85 dB	0,87 m
6 dBm	10 dB	2	25 GHz	-52,85 dBm	25 dBi	25 dBi	98,85 dB	8,71 m

Circuit Design Challenge:
trade-off Pout, NF, BW, range, antenna gain, packaging

Can we build a generic THz I/Q transceiver?

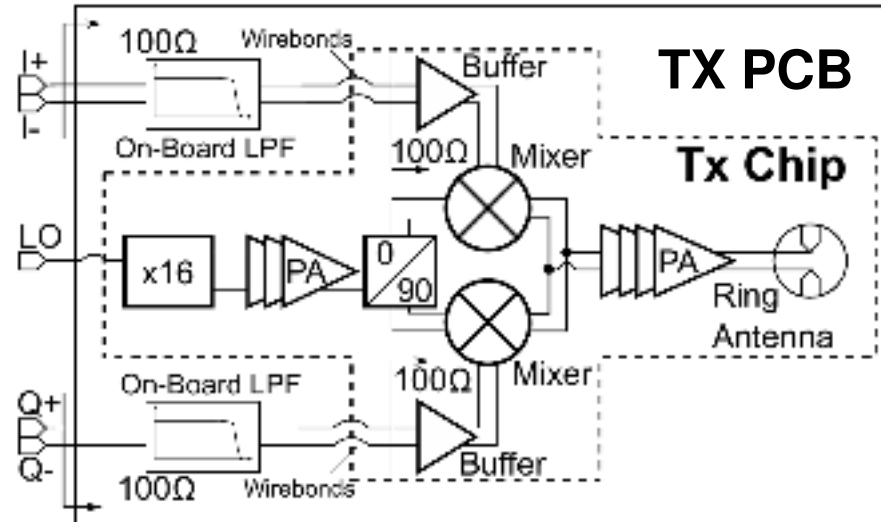
- **Circuit approach: generic wideband I/Q radios with spectral efficiencies of 2-3 bit/s/Hz at 240 GHz**



- Challenges: limitations in transmit power, receiver noise figure, IF/RF bandwidth, linearity and I/Q imbalance over a very wide bandwidth
- Approach: apply wide-band circuit matching techniques

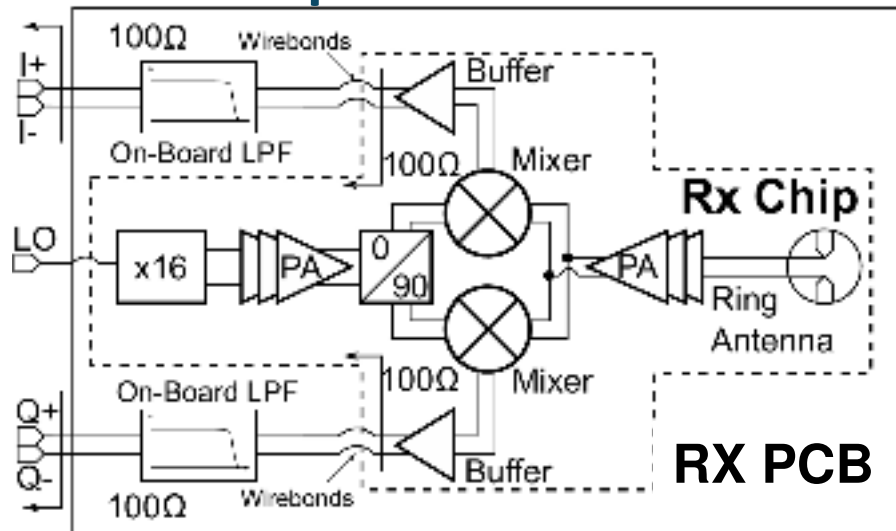
220-260 GHz TX/RX Chip Block Diagrams

Transmitter

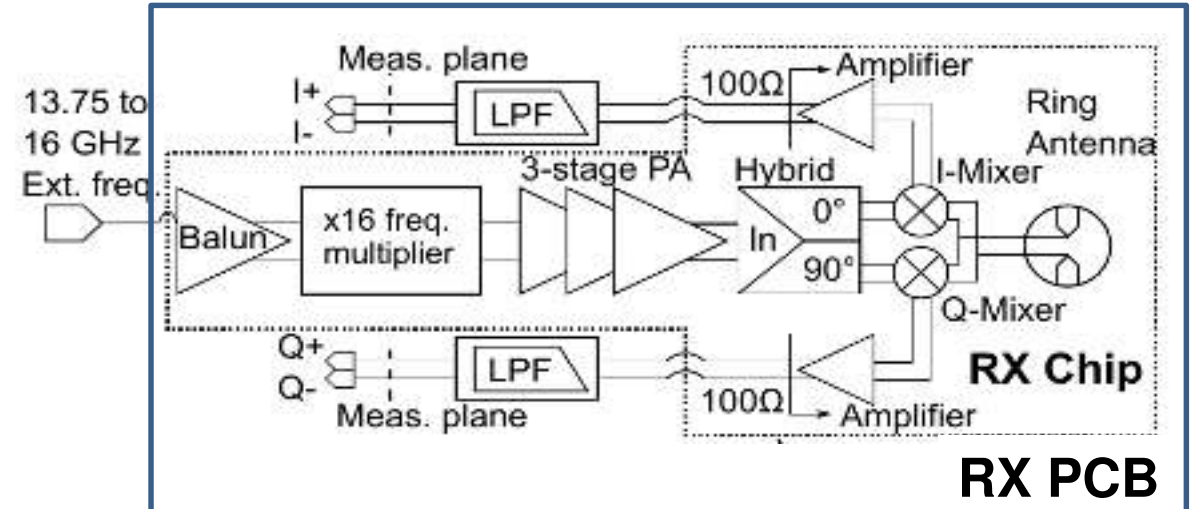


- [1] N. Sarmah et al, TMTT 2015 run 1
- [2] N. Sarmah et al, EUMIC 2016 run 2

Amplifier First



Mixer First

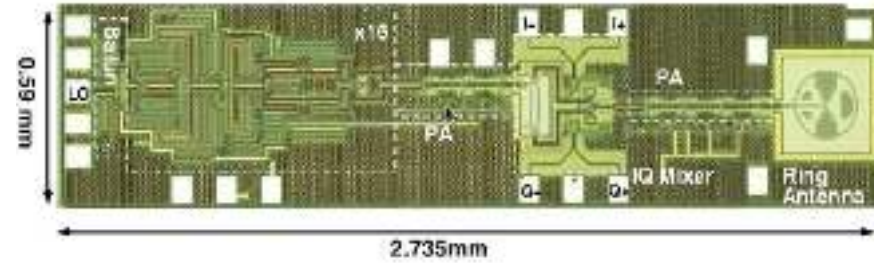


- [3] P. R. Vazquez et al, Int. J. of Microw. and Wireless Tech.

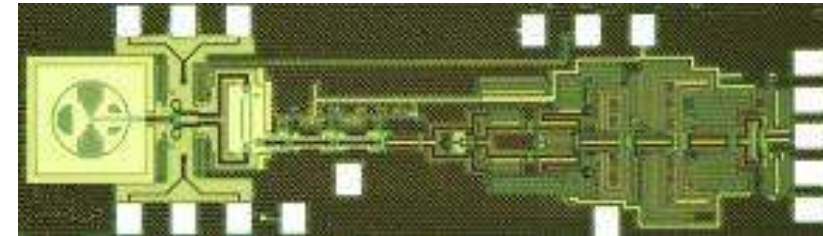
[1] N. Sarmah et al, TMTT 2015 run 1
 [2] N. Sarmah et al, EUMIC 2016 run 2

Chip Micrographs and Packaging

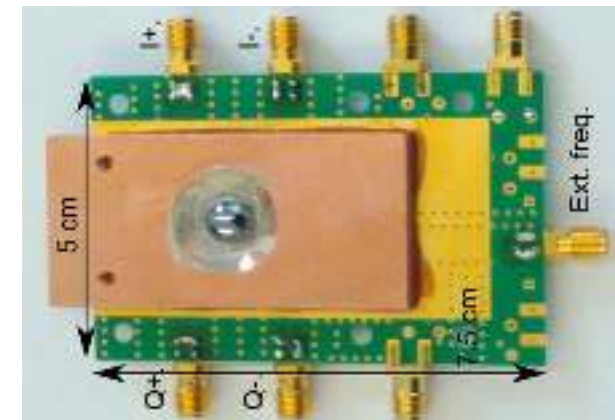
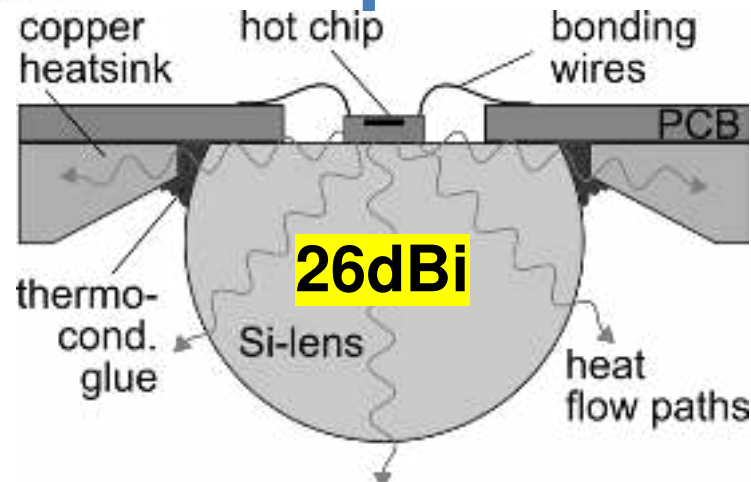
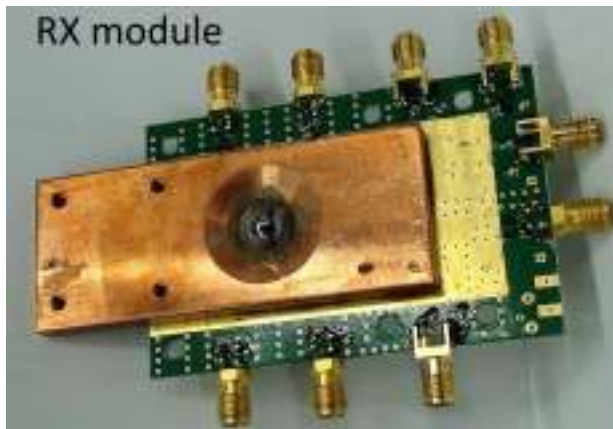
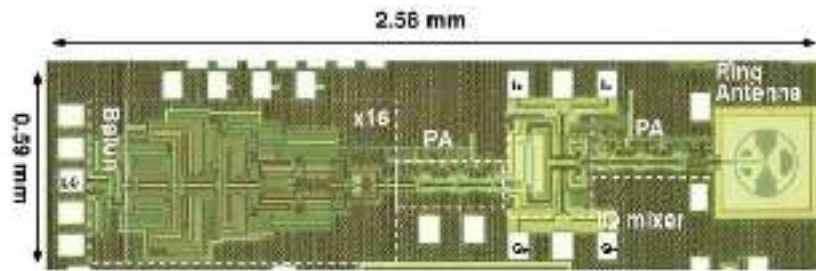
Transmitter



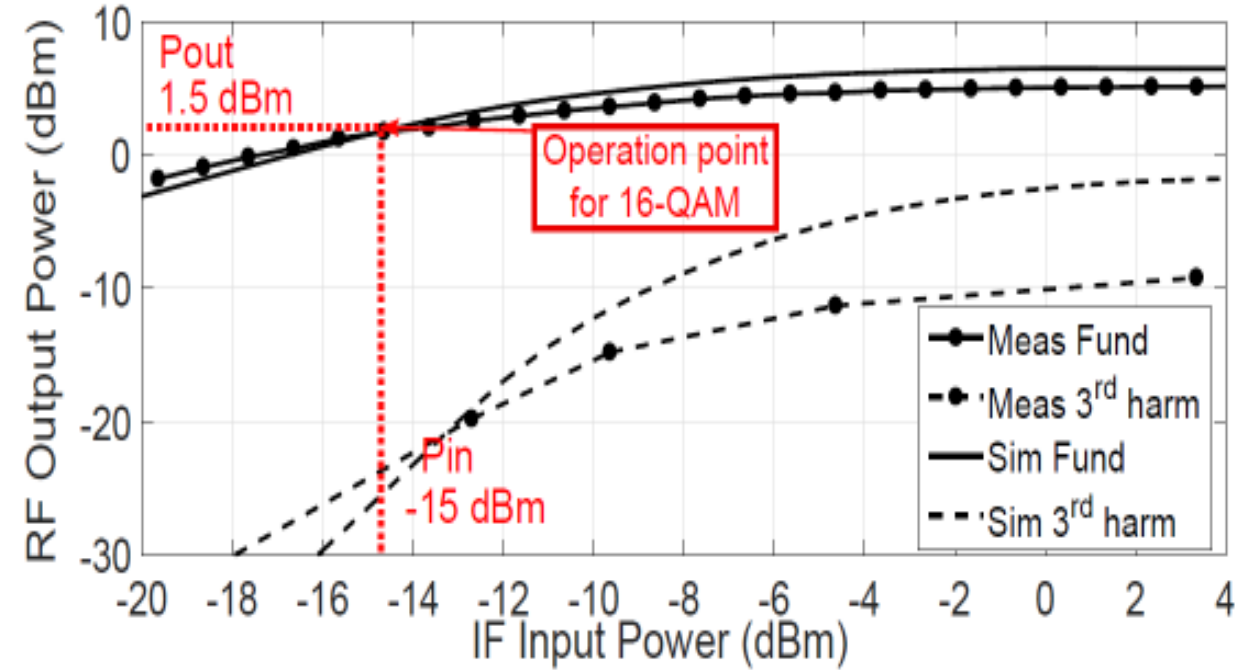
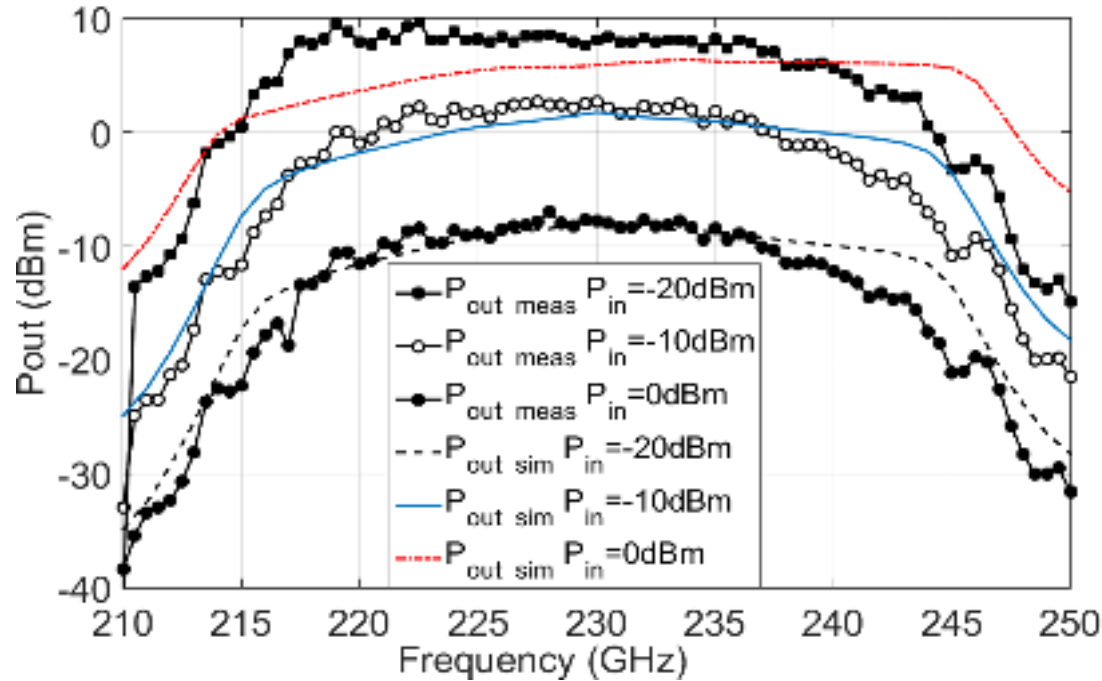
Mixer First RX



Amplifier First RX



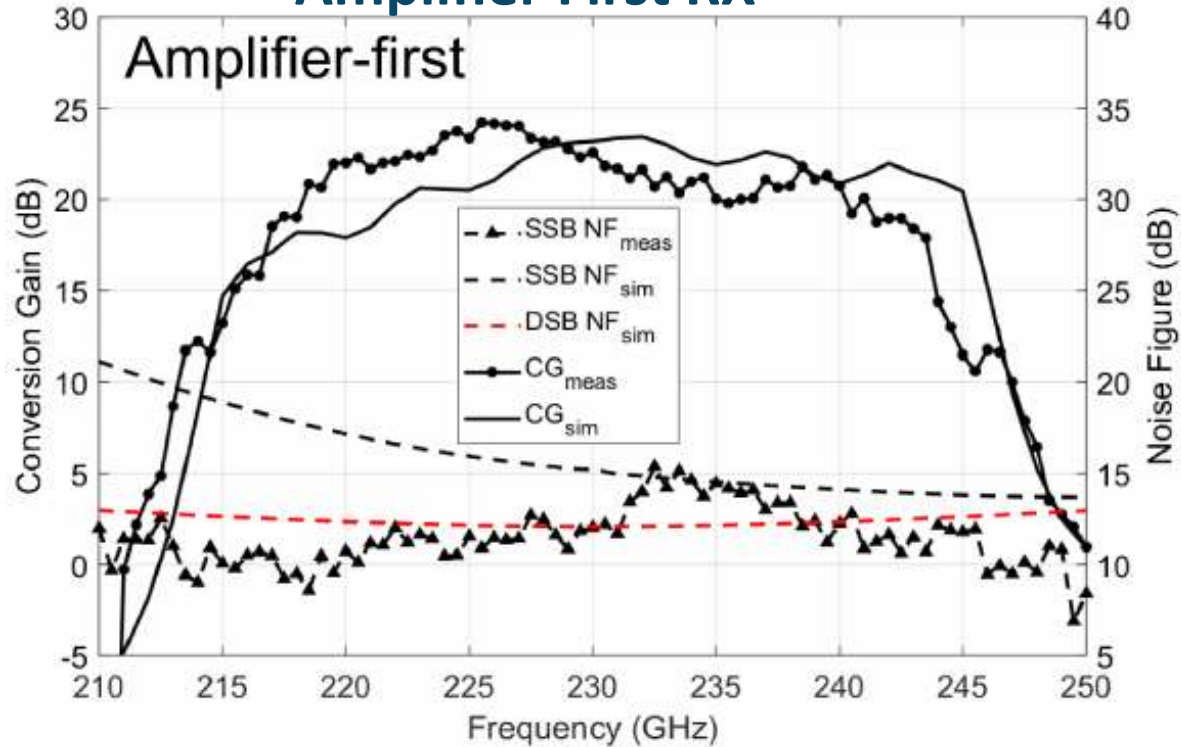
Transmitter RF Front-End Performance



- For LO = 220-260 GHz; P_{sat}= -2 to 9.5 dBm
- 3dB RF BW: 25GHz at 230GHz LO
- IP1dB = -15 to -5 dBm and additionally varies across IF frequency
- IQ Amp. Imb. < 0.5 dB for IF up to 17 GHz, IQ phase Imb. < 2 deg

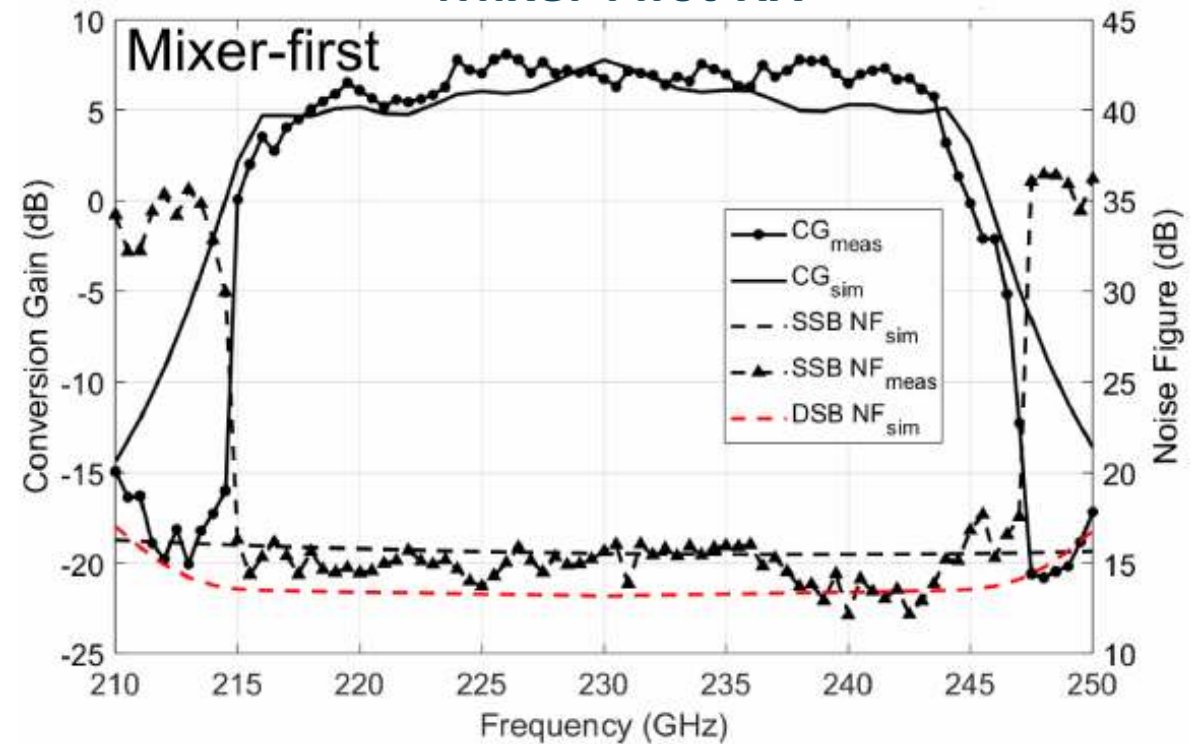
Receiver RF Front-End Performance

Amplifier First RX



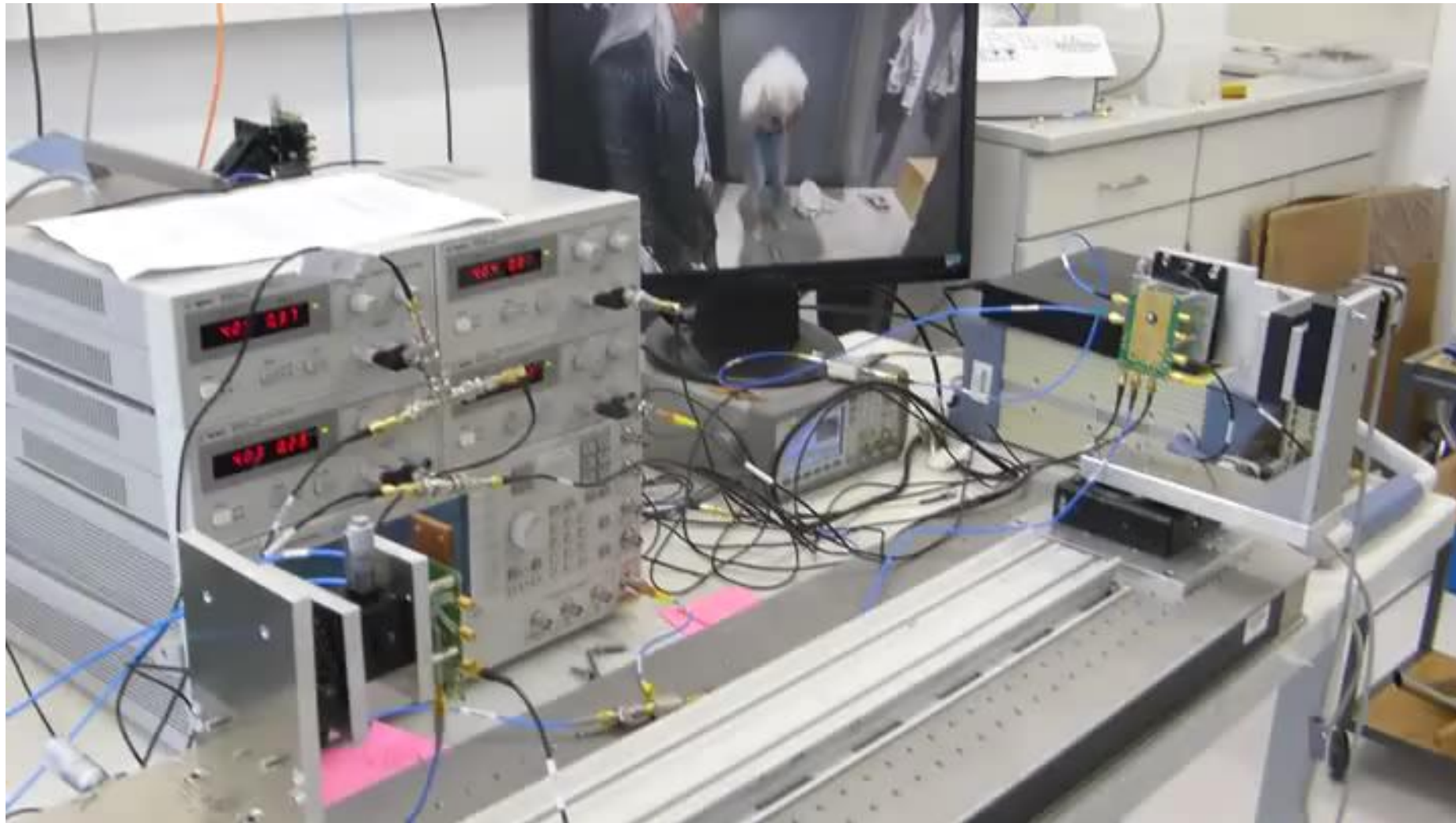
- For LO = 220-260 GHz:
 - CG = 12 to 24 dB, **SSB NF = 9 to 16 dB**
- 3dB RF/IF BW = **23/11.5 GHz**
- IQ Amp. Imb. < 0.5 dB for IF up to 17 GHz

Mixer First RX



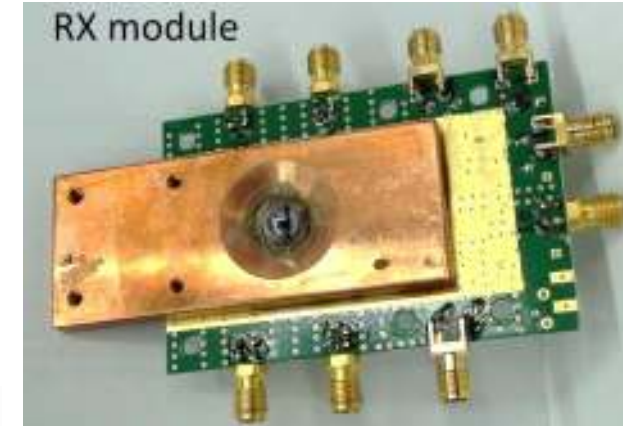
- For LO = 220-260 GHz:
 - CG = 7.8 dB, **SSB NF = 13.5 to 14 dB**
- 3dB RF/IF BW = **28/14 GHz**
- IQ Amp. Imb. < 1 dB for IF up to 17 GHz

Simple Communication Demo



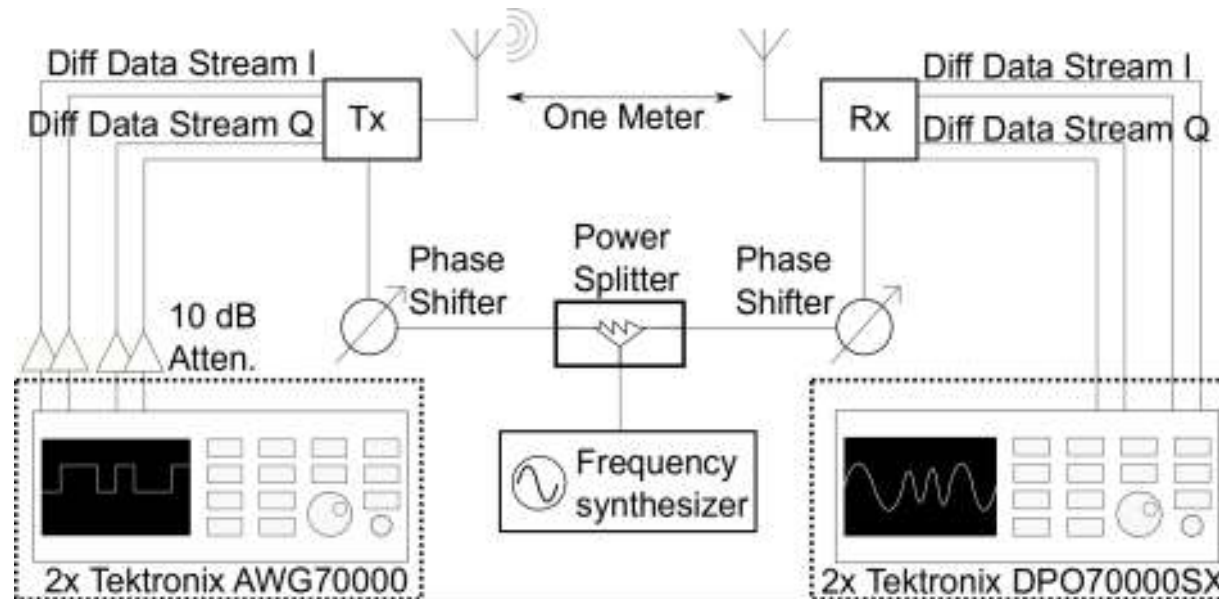
Testbed for 100 Gbps and Beyond?

- 1 meter line-of-sight
- No free space optics or mirrors
- LO Phase-shifters for phase alignment
- 10 dB IF attenuators for linear TX

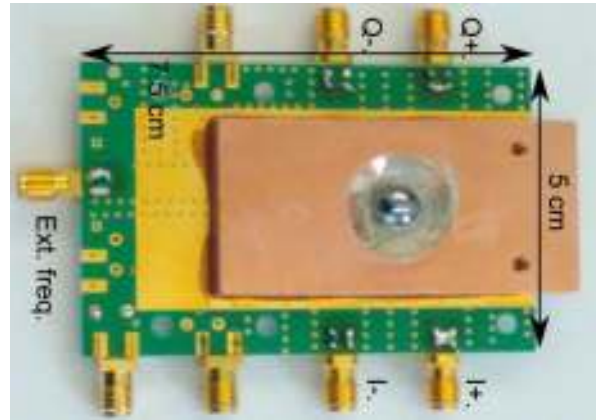


Scope:

- 2*33 GHz, 100 GS/s
- Vector signal analysis software
- RRC matches AWG
- Feed-forward adaptive equalizer (17 taps)



[1] P. R. Vazquez et.al. "Towards 100 Gbps: A Fully Electronic 90 Gbps One Meter Wireless Link at 230 GHz", European Microwave Conference (EuMC) 2018, :1389-1392 November 2018

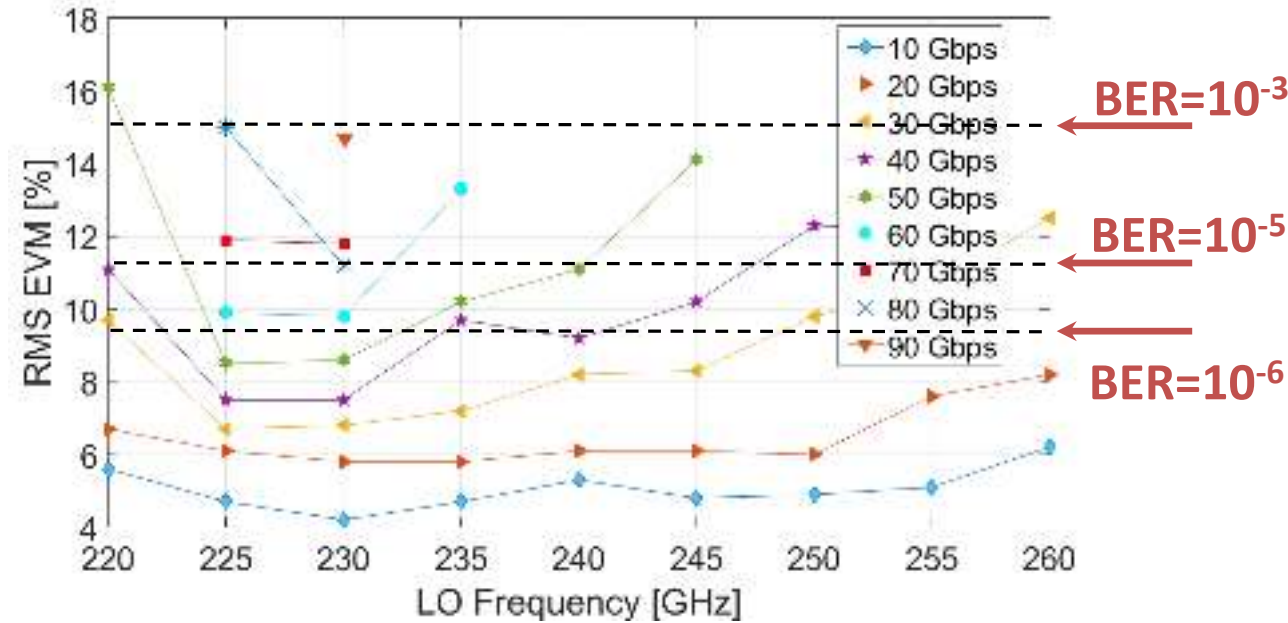


AWG:

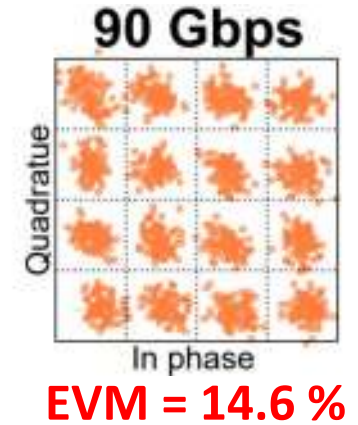
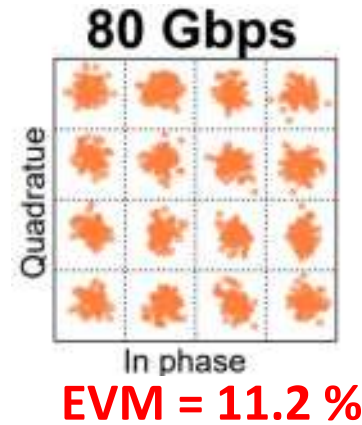
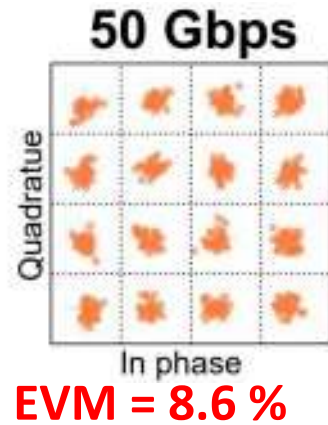
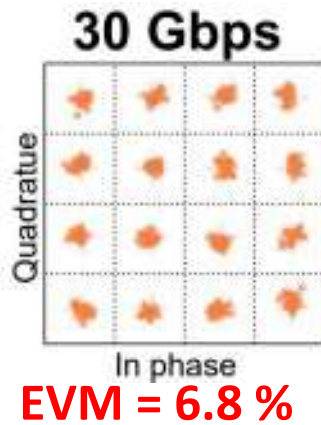
- RRC filter (0.1-0.7)
- Pre-compensation
- 50 GS/s and 10-bit
- 20 GHz analog BW
- Eff. BW 16QAM:
 - 12.4 GHz, 90 Gbps, -8.2 dBm, 2.5% EVM

Link Summary (Amplifier First)

Mod.	Date Rates/ EVM	Range/ max range	Reference
BPSK	35/27.5%	1m/5m	[RWW18]
QPSK	65/30.7%	1m/5m	[RWW18]
16QAM	90/14.7%	1m/1.8m	[EuMC18]
32QAM	90/11.9%	1m/1.6m	[APMC18]
64QAM	81/8.7%	1m/1m	[RWW19]



16QAM



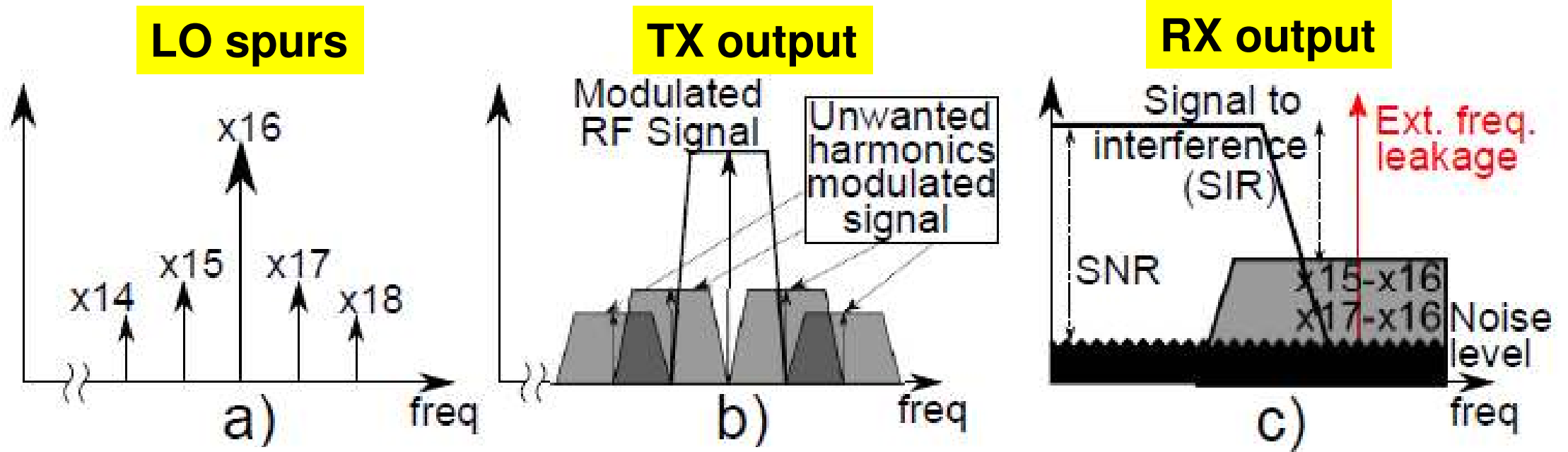
Limits: I/Q correlation, LO SFDR, -55 dB LO-BB feed-through, group delay distortion (package)

Chip-Set Summary (Tunable Carrier 220-260 GHz)

RF front-end performance	Amplifier First (230GHz carrier)				Mixer First (230GHz carrier)			
	Carrier/BW	Psat	CG	NFmin	Carrier/BW	Psat	CG	NFmin
	230GHz /24GHz	9dBm	23dB	11.5 dB	220-260 GHz /28 GHz	9dbm	7.8 dB	14 dB
Link performance	Mod.	Date Rates/ EVM	Range/ max range	Reference	Mod.	Data Rates/ EVM	Range/ max range	Reference
	BPSK	35/27.5%	1m/5m	[RWW18]	BPSK	35/27.9%	1m/4m	Not published
	QPSK	65/30.7%	1m/5m	[RWW18]	QPSK	60/26.2%	1m/4m	[IJMWT]
	16QAM	90/14.7%	1m/1.8m	[EuMC18]	16QAM	100/17%	1m/1.8m @ 80Gbps	[MWCL]
	32QAM	90/11.9%	1m/1.6m	[APMC18]	32QAM	90/13.7%	1m/1.6m	Not published
	64QAM	81/8.7%	1m/1m	[RWW19]				

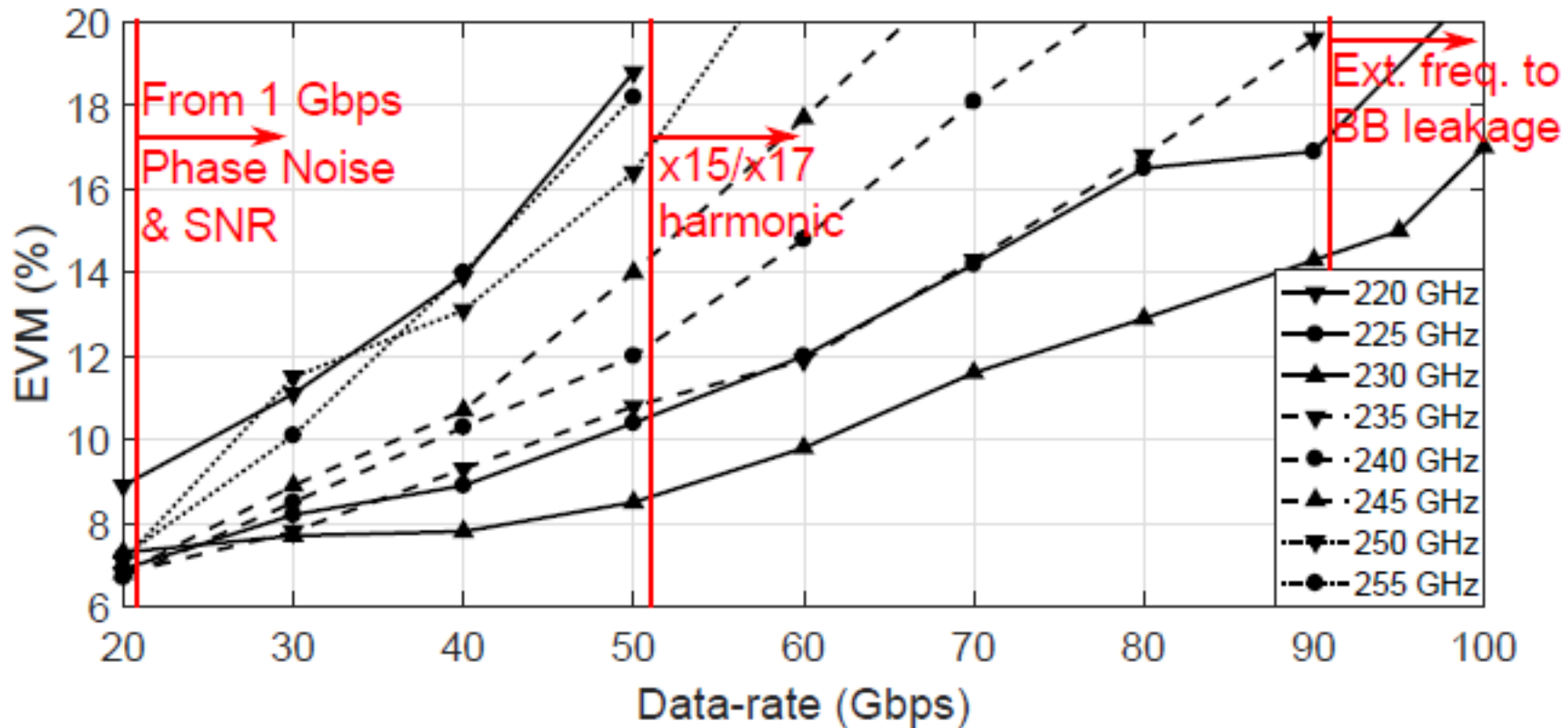
[MWCL] P. Rodríguez-Vázquez, et. al., "A 16-QAM 100-Gb/s 1-M Wireless Link With an EVM of 17% at 230 GHz in an SiGe Technology,"

Link Impairments (Mixer First)



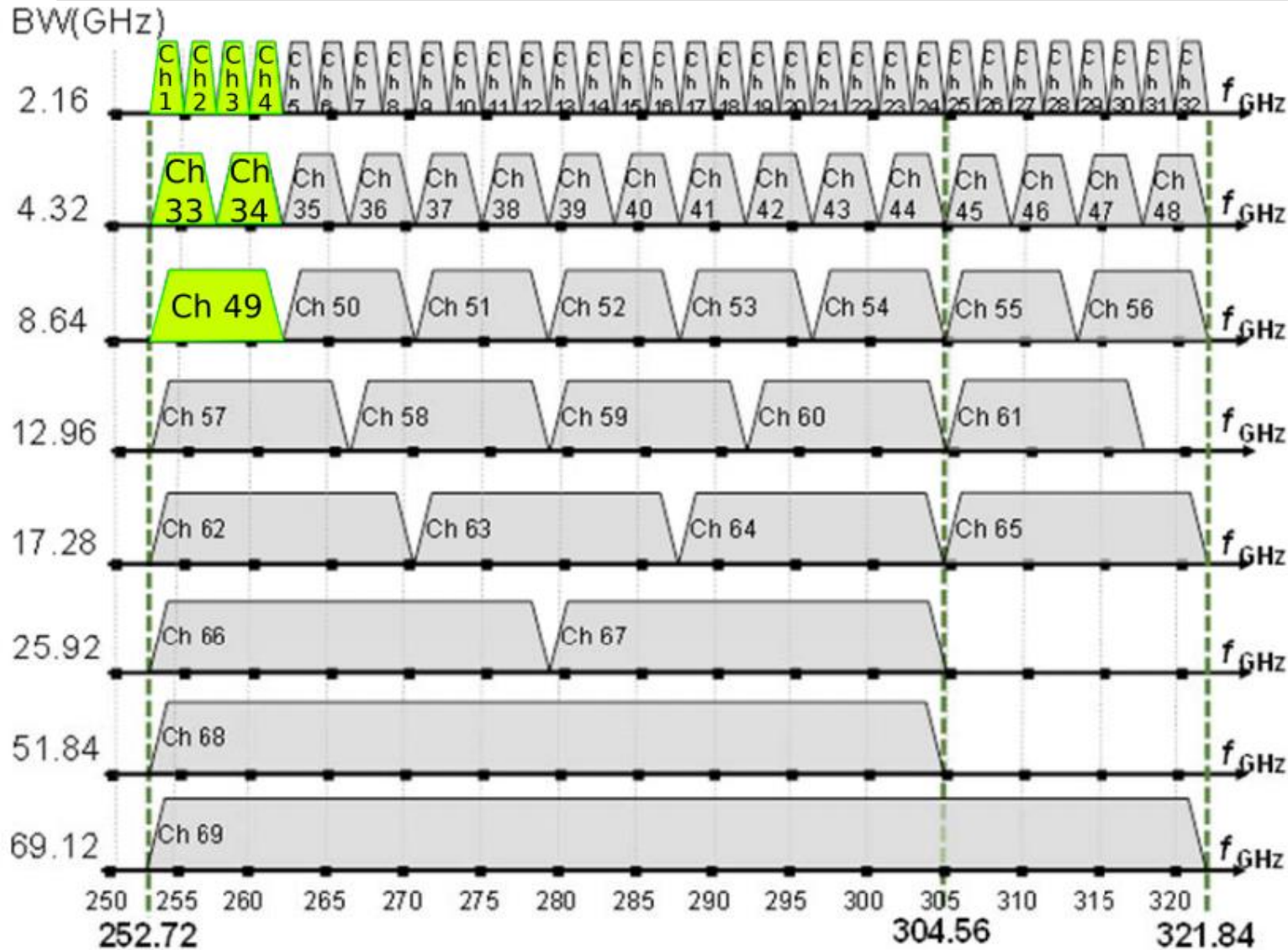
Tx to Rx Constellation 16-QAM			
	Symbol Rate	23.75 Gbaud	
EVM	15% / -16.4 dB	17% / -15.4 dB	
Data Rate	95 Gbps	100 Gbps	

Link Impairments (Mixer First)



- **Q: What do we need to improve data-rates and range?**
 - Range: Tx power, Rx noise figure, antenna directivity
 - Data rates: SNR/PN limit spectral eff., RF BW flatness, PN floor of ref. Synth, freq. planning

IEEE 802.15.3d-2017 Channel Allocation



- 4 Channels with 2.16 GHz BW @ 253.8, 255.96, 258.12, and 260.28 GHz
- 2 Channels with 4.32 GHz BW @ 254.88 and 259.2 GHz
- 1 Channel with 8.64 GHz BW @ 257.04 GHz
- All this Channels are expected to reach data-rates under 50 Gb/s. **We already reached this goal.**
- Link distance remains a problem:
 1. Pout 10 mW not 1 W
 2. Antennas 25 dBi not 40 dBi

More directivity is required (50 dBi to compensate for the reduced Pout)

Link Budget Estimation

Freq.	Tx Pout	RF BW	Data -rate	NF	Mod.	SNR for BER = 10^{-3} +16 dB loss	Antenna Gain (Tx & RX)	Power required in Rx	Maximum Distance	Notes
230 GHz	5 dBm	30 GHz	100 Gbps	14 dB	16-QAM	32.5 dB	26 dBi	-29 dBm	1 meters	Measured
230 GHz	5 dBm	30 GHz	100 Gbps	14 dB	16-QAM	32.5 dB	50 dBi	-29 dBm	100 m	With a second 6.5 cm lens

100m range expected for 50dBi lens gain

SoA for all-electronic wireless links < 200 GHz

Reference	Technology	Frequency	Channel BW	Modulation	Data-rate	P _{DC}	Distance	On- chip antenna	Fully-packaged?
[Kang15], [Thyagarajan15]	65 nm CMOS	240 GHz	-	QPSK	16 ¹ Gbps	480 mW	2 cm	2 Ring	No (on wafer)
[Fritsche17]	130 nm SiGe	190 GHz	20 GHz	BPSK	50 Gbps	154 mW ²	0.6 cm	Monopole	No (on wafer)
[Lee19]	40 nm CMOS	300 GHz	20 GHz	16-QAM	80 Gbps	1.79 W	3 cm	No	No (on wafer)
[Kallfass15]	35 nm InP	300 GHz	22 GHz	QPSK	64 Gbps	-	2 meters	No	Wave-guide
[Boes13]	35 nm InP	240 GHz	-	8-PSK	64 Gbps	-	40 meters	No	Wave-guide + Horn
[Hamada18]	80 nm InP	270 GHz	-	16-QAM	100 Gbps	-	2.2 meters	No	Wave-guide + Horn + Lens
[Eisa18]	130 nm SiGe	240 GHz	<15 GHz	BPSK	25 Gbps	950 mW	15 cm	Double folded dipole	PCB + plastic lens
[EUMC18]	130 nm SiGe	220-260 GHz	13 GHz	16/32-QAM	90 Gbps	1.96 W	1 meter	Ring	PCB + silicon lens
[MWCL19]	130 nm SiGe	220-255 GHz	13 GHz	16-QAM	100 Gbps	1.41 W	1 meter	Ring	PCB + silicon lens

¹ Tx without baseband interface: PRBS generator on chip.

² No LO generation path implemented on chip.

Conclusion and summary

- **Vast number of potential applications for Silicon at mmWave/THz**
 - THz Video Cameras, 3D radar
 - Near-field imaging and sensing in biomedical applications
- **A 100 Gbps wireless communication is possible in SiGe today!**
 - 240GHz Chipset and with a inexpensive COB packaging incl. Si lense
 - Up to **100m** range with 50dBi antenna gain possible
- **Future challenges**
 - Improve on RF link impairments
 - Solve the digital base-band gap

Close the 6G and technology gap!

Thanks



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- DFG Reinhart Koselleck Projekt, PF 661/11-1
- DFG PF 661/6-1, LO 455/22-1, and 661/10-1



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